



Sir:

PATENT Customer No. 22,852 Attorney Docket No. 04329.3298-00

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:		)	
	Yoshio OZAWA et al.	)	
Applic	ation No.: 10/812,987	)	Group Art Unit: 2822
Filed:	March 31, 2004	)	Examiner: Mark V. PRENTY
For:	SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE SAME	) ) )	Confirmation No.: 5987
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450			

## RESPONSE TO RESTRICTION REQUIREMENT

In a restriction requirement mailed on August 4, 2005, the period for response to which extends through September 6, 2005, the Examiner required restriction under 35 U.S.C. § 121 between:

Group I: Claims 1-4, characterized by the Examiner as drawn to a semiconductor device and classified in class 257, subclass 316; and

Group II: Claims 5-20, characterized by the Examiner as drawn to a method of making a semiconductor device and classified in class 438, subclass 257.

Applicants elect to prosecute Group II, claims 5-20, without traverse.

Please grant any extensions of time required to enter this response and charge any additional required fees to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated: September 6, 2005

Reece Nienstadt

Reg. No. 52,072

**RVB/RWN**